

Class	Subclass

ISSUE CLASSIFICATION

SCANNED 2

CPA

01-1082

UTILITY SERIAL NUMBER	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 08/897,953	FILING DATE 07/24/97	CLASS 431	SUBCLASS 1	GROUP ART UNIT 28-1	EXAMINER J. H. [Signature]
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CONTINUING DATA**
VERIFIED THIS APPLN IS A CON OF 08/393,677 07/24/97

FOREIGN/PCT APPLICATIONS**
VERIFIED JAPAN 6-088762 04/26/97

CPA

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY JPX	SHEETS DRWGS. 6	TOTAL CLAIMS 10	INDEP. CLAIMS	FILING FEE RECEIVED \$12,000.00	ATTORNEY'S DOCKET NO. 2501076
Verified and Acknowledged		Examiner's Initials						

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TITLE: METHOD AND APPARATUS FOR FABRICATING SEMICONDUCTOR DEVICE

U.S. DEPT. OF COMM. / PAT. & TM. — PTO-436L (Rev. 12-94)

PARTS OF APPLICATION FILED SEPARATELY				Applications Examiner	
NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
Label Area		PREPARED FOR ISSUE		ISSUE BATCH NUMBER	
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

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